



PATENT

PATENT AND TRADEMARK OFFICE

Applicant:

BARTLEY et al.

Application: METHOD AND APPARATUS FOR IMPLEMENTING SILICON WAFER

CHIP CARRIER PASSIVE DEVICES

Serial No.:

10/787,478

Filing Date: February 26, 2004

Art Unit:

2125

Examiner:

Steven R. Garland

Case:

ROC920030354US1

CERTIFICATE OF MAILING: I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed for Commissioner of Patents, P.O. Box 1450, Alexandria, VA, 22313-1450, on

Date of Deposit

Joan Pennington

Typed/printed name of person signing

535 North Michigan Avenue Unit 1804 Chicago, Illinois 60611

Mail Stop Amendment **Honorable Commissioner Of Patents** P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Official Action mailed September 28, 2005, please amend the above-identified application as follows: